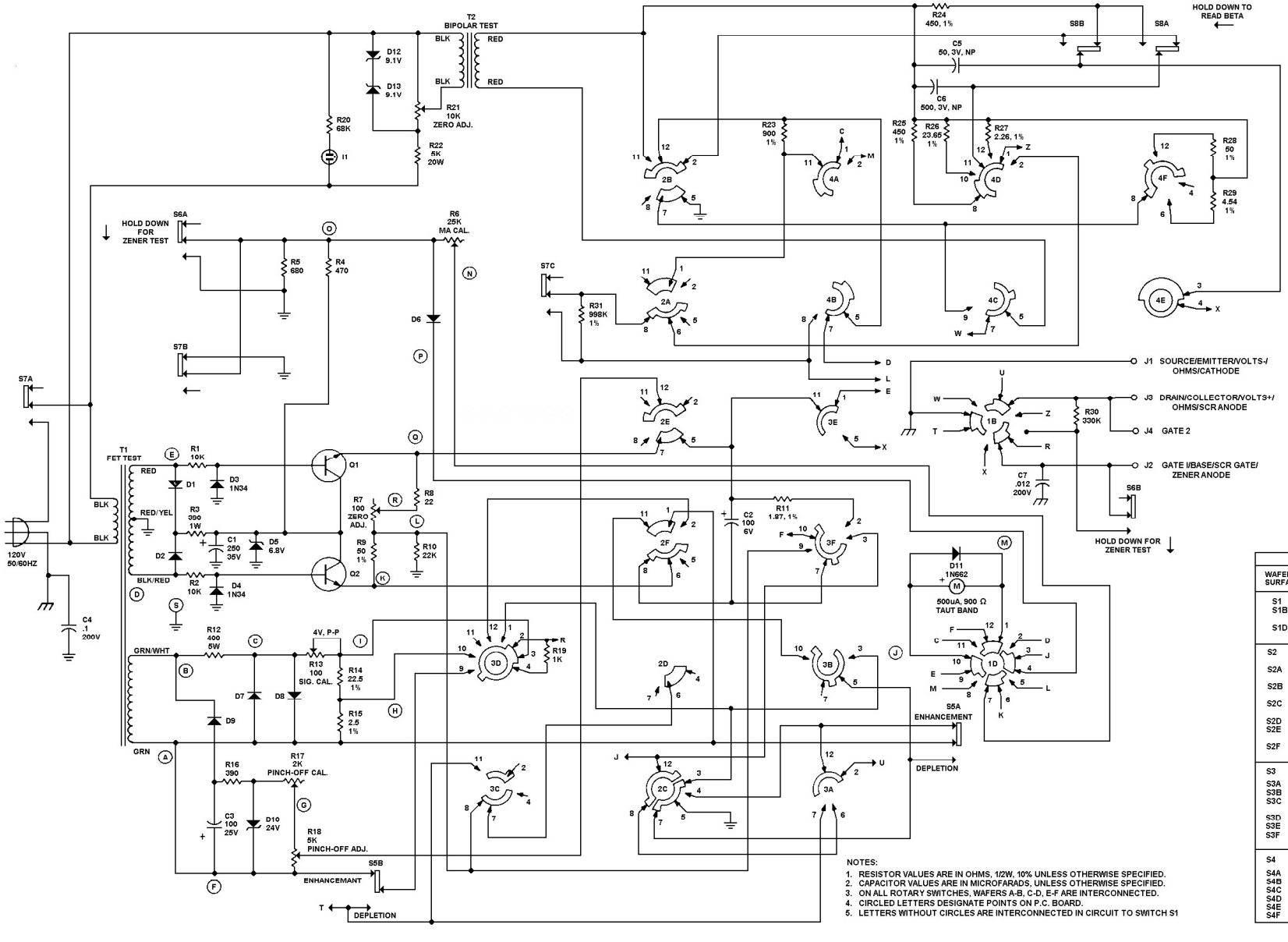

S1 - ROTARY SWITCH

POS 1 - FET/UJT/DIODE/SCR/ZENER
POS 2 - BIPOLAR/VOLTS/OHMS

S2 - ROTARY SWITCH

POS 1 - N-CH/NPN/OHMS/DIODE ON/ZENER
POS 2 - P-CH/PNP/VOLTS/DIODE OFF/SCR

S3 - ROTARY SWITCH

POS. 1 - g_m X1
POS. 2 - g_m CAL
POS. 3 - g_m X10
POS. 4 - I_{DSS} /DIODE
POS. 5 - V_P /SCR/ZENER
POS. 6 - I_{GSS} /UJT

S4 - ROTARY SWITCH

POS. 1 - RF X1
POS. 2 - SIG X1
POS. 3 - SIG X10
POS. 4 - PWR X1
POS. 5 - PWR X10
POS. 6 - I_{CBO}
POS. 7 - I_{CEO} VOLTS/OHMS

S5 - SLIDE SWITCH

POS 1 - ENHANCEMENT
POS 2 - DEPLETION

S6 - SLIDE-SPRING RETURN SWITCH

PUSH FOR ZENER TEST

S7 - SLIDE SWITCH

POS 1 - VOLTS/POWER OFF
POS 2 - POWER ON

S8 - SLIDE-SPRING RETURN SWITCH

PUSH TO READ BETA

	POSITIONS						
WAFER SURFACE	1	2	3	4	5	6	7
S1 S1B S1D	FET 1-2 5-6 9-10 1-12 3-4 6-7 9-10	BIPOLAR 2-3 6-7 10-11 1-2 4-5 7-8 10-11					
S2 S2A S2B S2C S2D S2E S2F	N-CH NPN 1-11 6-8 2-12 5-7 3-5-7 8-12 4-6 2-12 5-7 1-11 6-8	P-CH PNP 1-2 5-6 7-8 11-12 3-12 4-5-8 6-7 7-8 11-12 1-2 5-6					
S3 S3A S3B S3C S3D S3E S3F	g_m X1 2-12 5-7 7-6 2-3-4 1-11 7-10	g_m CAL 2-12 5-7 7-8 3-4-9 1-11 7-10	g_m X10 2-12 5-7 2-3 7-8 4-10 1-11 7-10	I_{DSS} 2-12 5-7 7-6 4-11 1-11 2-7-9-10	V_P 2-6 3-7-10 4-12 1-5 3-10	I_{GSS} 2-6-7 4-1 1-5 3-10	
S4 S4A S4B S4C S4D S4E S4F	RF X1 1-11 5-7 5-7 1-6-11 3-4	SIG X1 1-11 5-7 5-7 1-10-11 3-4 4-12	SIG X10 1-11 5-7 5-7 1-10-11 3-4	PWR X1 1-11 5-7 5-7 1-11-12 3-4 4-6	PWR X10 1-11 5-7 5-7 1-11-12 3-4 4-12	I_{CBO} 1-2 5-6 1-2 3-4	I_{CEO} 1-2 5-6 7-9 1-2

NOTES:

1. RESISTOR VALUES ARE IN OHMS, 1/2W, 10% UNLESS OTHERWISE SPECIFIED.
2. CAPACITOR VALUES ARE IN MICROFARADS, UNLESS OTHERWISE SPECIFIED.
3. ON ALL ROTARY SWITCHES, WAFERS A-B, C-D, E-F ARE INTERCONNECTED.
4. CIRCLED LETTERS DESIGNATE POINTS ON P.C. BOARD.
5. LETTERS WITHOUT CIRCLES ARE INTERCONNECTED IN CIRCUIT TO SWITCH S1

EICO 685 SCHEMATIC DIAGRAM